

Optimal Bidirectional Power Flow in Probabilistic Solar Conditions Using a Robust Nonlinear Control Approach

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Abstract: The utilization of solar energy for power stations has gained popularity in recent years. However, due to unpredictable weather conditions and solar irradiance, relying solely on solar energy for power supply throughout the day is not feasible, resulting in an inconsistent power supply to the load. To address the abovementioned issues, this paper addressed by proposing a two-stage grid-connected solar PV system that incorporates an enhanced energy balance control approach that adequately balances the power flow delivery to the load with constant voltage and a properly charged battery. The implementation of the proposed work is evaluated in two stages. Firstly the maximum power point algorithm (MPP) based on POASMC (Perturb & observe adaptive sliding mode control) is used for stochastic solar PV, then integration of an energy storage system with solar PV through a PI controller. Moreover, in the secondary stage, a power flow control logic is proposed and implemented to synchronize solar PV with the grid and analyze the grid-connected PV performance, taking into account the state of charge (SOC) and load current.

Keywords: Photovoltaic systems, Maximum power point technique (MPPT), POASMC, Grid connected system, Hybrid energy storage, Power management,

1. Introduction

The use of technology and industrial equipment has increased dramatically in recent decades; as a result, the demand for energy supply increased rapidly. To fulfil these energy supply demands by coal and fossil fuel-based energy generation system itself creates a negative impact on the environment i.e., global warming, pollution, climate change, and environmental degradation [1] [2]. Therefore, experts and researchers from around the world are investigating alternate energy solutions to address the challenge mentioned above. [3][4].

Renewable energy sources, i.e., in the form of wind energy, solar energy, and geothermal, are gaining popularity as an efficient and viable alternative to traditional energy sources. These energy sources are naturally replenished and do not run out, making them sustainable for energy generation [3]. Among these, wind and geothermal-based energy generators are location-specific and may not be suitable for every location. In contrast, solar energy-based photovoltaic (PV) systems are especially attractive due to their extensive availability, cost-effectiveness, and advanced technology.



However, it is essential to note that PV systems are susceptible to changing environmental circumstances and temperatures, which can affect their efficacy[5] [6][7].

In a solar PV system, when solar insolation changes, the output power of solar PV changes abruptly, impairing solar PV efficiency. To solve this problem, an effective MPPT control algorithm can be used to ensure that solar PV operates at MPP. Many research studies have been conducted to propose different MPPT control algorithms, with perturb & observe (P&O) and incremental conductance (InC) being the most common due to their simplicity and easy understanding [8][9][10]. However, these algorithm techniques for MPP have a few limitations, such as oscillations nearby the peak power and failure to track maximum power due to rapid changes in the environmental conditions. To address these limitations, researchers have designed other controller techniques, including intelligent control techniques [11][12], conventional control techniques [13], sliding mode (SMC) [14], fractional order SMC [15], swarm-based techniques [16] [6][17], and combination of the controller with optimization techniques [18-21].

Due to the sun's unavailability at night, the irregularity of solar radiation, and the intermittent nature of the PV array, a hybrid renewable energy source with battery storage (PV-BSE) have become necessary for continuous and reliable output power operation. As a result, a renewable energy source with battery storage (PV-BSE) has been developed to provide steady and reliable output power [22]-[25]. Although a limited storage capacity of the battery, it is only possible to provide backup some of the time [26]. Therefore, to obtain continuous power supply at load, if solar energy is not accessible and there are circumstances for energy storing discharge, a grid-connected PV-BES (due to limited availability of grid in the rural area) has been designed and integrated the PV-BES system with a single-phase grid complies with a standard DC bus system [27][7][24]. To maintain the system stability and constant power supply at load with minimum deficiency of power, a power flow management logic has been implemented based on a RESs energy system, battery SOC, and grid current [19][28][29].

Two sub-controllers and one synchronization device have been used for grid-side management. On the grid side, a current controller accurately injects the reference current into the grid through an active/reactive (PQ) controller that is predicated on the necessary P and Q [30] [31]. A phase lock loop (PLL) has been used to synchronize and regulate [32] the grid system with the load demand. The synchronization unit also referred to as the system's center part and is responsible for collecting grid voltage information, i.e., grid frequency, phase, and magnitude.

From the cited literature, it is noticed that there needs to be more research for the situations where there is an adequate power supply from the generation side to meet load demand on the DC bus. So for that situation, mainly when DC load demand is time-varying, and it is integrated with the grid, battery, and renewable energy-based solar PV. To resolve the abovementioned challenges, this article discusses and contributes to a two-stage energy management system based on grid current, battery SOC, and solar power generation. The POASMC MPPT technique is suggested and developed in the first step to get the maximum power energy from PV. The proposed POASMC control approach is further divided into two stages. The initial stage utilizes P&O to generate a reference voltage and control the voltage regulation in an MPPT controller. Furthermore, in the second stage, an upgraded PIDSM controller enforces the solar PV voltage to follow the reference voltage and generates the duty ratio to achieve optimal exploitation of solar PV under varying insolation. The POASMC approach was thoroughly compared with traditional techniques for performance evaluation. Furthermore, for continuous power supply to the load on the DC bus, a PI controller-based energy management strategy for energy distribution based on SOC of battery, power generation of solar and grid current proposed and implemented in the second stage.

The paper presents three significant contributions: Firstly, it introduces the POASMC MPP technique, which is designed and implemented for a grid-connected system capable of handling periodic solar variations in insolation levels from 20 to 1000 W/m² at 25 °C. Secondly; the paper evaluates the efficacy of the suggested POASMC by comparing the output of a 1602 W photovoltaic system with the simulation. Lastly, the authors propose a power flow management algorithm based on renewable energy power generation, battery SOC, and grid current to maintain a constant supply to the load in a stochastic environment. To present the research in a structured manner, the article is divided into different sections. Section 2 describes the general configuration of the proposed system. In Section 3, the

paper discusses the DC-DC control, which includes a detailed design description of the boost converter, mathematical modelling of the PV system and MPPT control techniques. Section 4 presents the grid-connected system with a battery and its power flow control algorithm. In Section 5, detailed simulations result and discussion are presented. Finally, the conclusion is shown in the last section.

2. Proposed System Configuration

Figure 1 depicts the typical design of the Solar PV integrated grid charging station used and described in this paper. The design involves using MPPT controller methods with a DC-DC converter to maximize the output of solar PV. To ensure maximum solar PV energy and match the load and source impedance, the boost converter's duty cycle is controlled by a P&O-based sliding mode controller (SMC). The boost converter generates a constant voltage of 350 V for solar PV systems and is connected to a bus system. A DC load, such as a vehicle charging

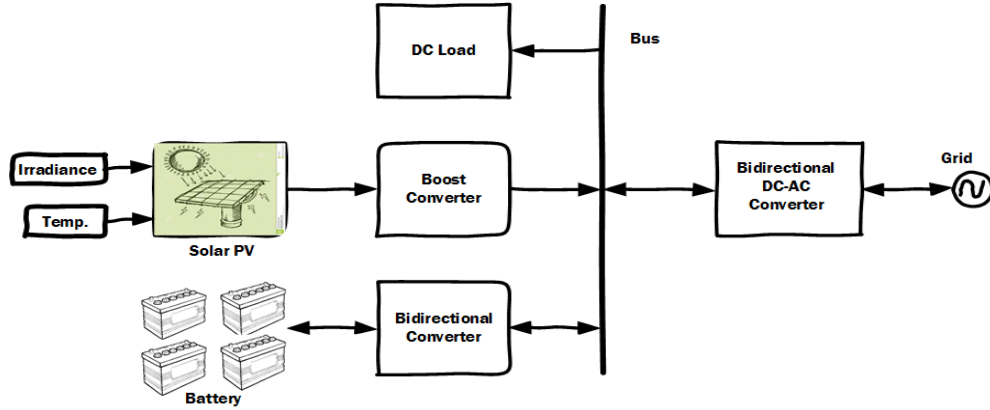


Fig. (1) General architecture of the Solar PV integrated grid charging station

system is connected to the bus system, and a bidirectional converter connects a battery storage device to the DC bus. Additionally, a bi-directional DC-AC converter that generates a after-loss 325 V connects the DC bus system to the single-phase grid.

2.1 Solar PV system

Solar PV cells can be modeled for electrical circuit applications using different techniques, with the one-diode module being a popular choice for designing solar PV systems. Equation (1) presents the equation of the solar PV module.

$$I_{total} = I_{phc} - I_{RSc} \left\{ e^{q_{ed} \frac{(V_{pv} + R_s I_{pv})}{AK_{bc}T}} - 1 \right\} \frac{V_{pv} R_s I_{pv}}{R_{sh}} \quad (1)$$

Where, I_{phc} is photodiode current, I_{RSc} reverse saturation current of diode, $e^{q_{ed}}$ is electron charges, V_{pv}, I_{pv} are PV voltage and current, A denoted as fitting curve factor, R_s, R_{sh} is series and shunt resistance, T is temp. , and K_{bc} value is taken as $[1.38 * 10^{-23} J/K]$.

It can be inferred from equation (1) that R_{sh} a solar PV has a ∞ value, and the I-V slope curve will be equal to 0 at short circuit conditions. Therefore, in the context of photodiode current, the short circuit current can be substituted by it.

$$I = I_{sc} - I_{RSc} \left\{ e^{q_{ed} \frac{(V_{pv} + R_s I_{pv})}{AK_{bc}T}} - 1 \right\} \quad (2)$$

For application specific, the solar PV module written in eq. (2) can be considered as $\left(e^{q_{ed} \frac{(V_{pv} + R_s I_{pv})}{AK_{bc} T}}\right)$ and can be replaced by $\left(e^{q_{ed} \frac{(V_{pv} + R_s I_{pv})}{AK_{bc} T n_s}}\right)$, where n_s are crystalline solar cells. Therefore, by putting the above values in equation (2), it can be rewritten as:

$$I = I_{sc} - I_{Rsc} \left\{ e^{q_{ed} \frac{(V_{pv} + R_s I_{pv})}{AK_{bc} T n_s}} - 1 \right\} \quad (3)$$

Rewriting equation (3) as when the PV cell operating in an open circuit, i.e., $I=0$

$$\frac{e^{q_{ed}}}{n_s AK_{bc} T} = \frac{\ln\left(\frac{I_{sc}}{I_{Rsc}} + 1\right)}{V_{oc}} \quad (4)$$

Therefore, the solar PV current can be thought of as following equations (3) and (4). $I = I_{sc} \left\{ 1 - \frac{I_{Rsc}}{I_{sc}} \left(e^{\ln\left(\frac{I_{sc}}{I_{Rsc}} + 1\right) \frac{(V_{pv} + R_s I_{pv})}{V_{oc}}} - 1 \right) \right\}$ (5)

Taking $z_m = \frac{I_{sc}}{I_{Rsc}}$, then equation (5) will be

$$I = I_{sc} \left\{ 1 - \frac{1}{z_m} (z_m + 1) \frac{(V_{pv} + R_s I_{pv})}{V_{oc}} + \frac{1}{\zeta} \right\} \quad (6)$$

Given that the solar PV is considerably bigger than the I_{Rsc} , then ζ will be high then $\frac{1}{\zeta}$ is negligible. Therefore, equation (6) will be

$$I = I_{sc} \left(1 - \zeta \frac{(V_{pv} + R_s I_{pv})}{V_{oc}} - 1 \right) \quad (7)$$

2.2 Boost Converter Design

The boost converter in the circuit is utilized to increase the input voltage and act as an interface between the load and the photovoltaic system, especially when there are changes in irradiance and weather conditions. An MPPT control method can be used to modify the boost converter's duty ratio "D," enabling the maximum amount of solar power to be supplied to the load through switch S, ensuring that solar power extraction operates continuously at its highest level of power. The boost converter's performance is expressed in terms of duty cycle "D" [33].

$$P_{Vv} = \frac{1}{C_{in}} \left[P_{Vv} - i_{load} \right] \quad (8)$$

$$i_{load} = \frac{1}{L} [P_{Vv} - V_o + V_o * D] \quad (9)$$

Where, i_{load} , P_{Vv} are the variable states, D represents the converter duty cycle, V_o is the output voltage, P_{Vr} depicts dynamic resistance, and it can be represented as $P_{Vr} = \frac{\partial P_{Vv}}{\partial P_{Vi}}$.

3. Maximum power point (MPP) algorithm

The main drawback of a commercial Solar PV system is its poor conversion efficiency. As a result, the MPPT algorithm is used to improve system efficiency. Since the MPPT is an essential component of PV systems, the duty cycle regulation is used to manage the Solar PV output. In this regard, to operate solar power at its maximum, a reference voltage uses to generate the required pulses for pulse width modulation (PWM).

A solar photovoltaic (PV) system's efficiency is a crucial aspect, and one of its major drawbacks is its low rate of conversion. To address this issue, MPPT techniques are employed to enhance the efficiency of the system. One of the essential components of a solar PV system is MPPT, and it is regulated by duty cycle control. To ensure that solar

power is running at its maximum efficiency, a reference voltage is utilized to generate the required pulses for pulse width modulation (PWM). Therefore, PWM is employed to regulate the output of solar PV systems.

3.1 Incremental conductance (InC) technique

Start

Step-1: Read solar inputs voltage and current $V_{pv}(n)$ and $I_{pv}(n)$

Step-2: Evaluate total power $P_{pv}(n) = V_{pv}(n) * I_{pv}(n)$

Step-3: Call P_{pv} and V_{pv} earlier values, i.e., $P_{pv}(n-1)$ and $V_{pv}(n-1)$

Step-4: Calculate ' ∂p ' and ' ∂v ' using

$$\partial v = V_{pv}(n) - V_{pv}(n-1) \text{ and } \partial p = P_{pv}(n) - P_{pv}(n-1)$$

Step-5: If $\partial v = 0$, Then no change in D and jump to Step 7

$$\text{Step-6: else If } \partial I > \left[\frac{\partial i}{\partial v} + \frac{1}{v} \right] < \varepsilon$$

$\frac{\partial i}{\partial v} > -\frac{1}{v}$, increase D by ΔD and jump to Step 9.

else decrease the D by ΔD and jump Step 9.

Step-7: If $\left[\frac{\partial i}{\partial v} + \frac{1}{v} \right] < \varepsilon$ then jump to Step 9

Step-8: Else if $\frac{\partial i}{\partial v} > -\frac{1}{v}$, the ΔD increase and jump to step 9, else ΔD decrease.

Step-9: Calculate $V_{pv}(n)$ and $I_{pv}(n)$

Return.

3.2 Proposed POASMC technique

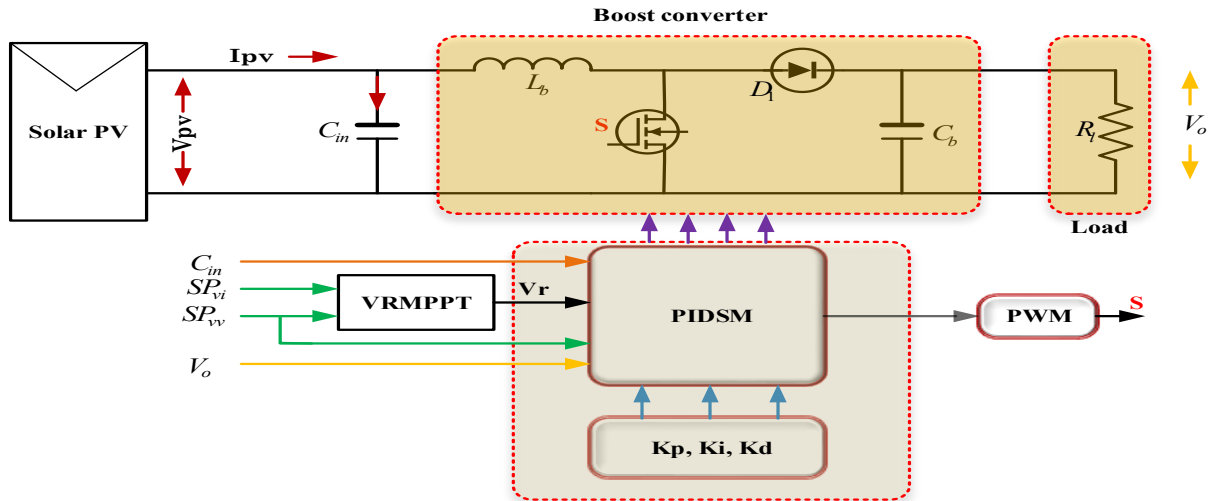


Fig. (2) Block diagram of the Proposed POASMC technique

As previously mentioned in equation (8-9), in boost converter design, a suggested cascade structured PIDSM (PID sliding mode) voltage regulator provides the required D of the signal, driving the boost circuit toward the

reference voltage. Therefore, to increase solar PV power, PIDSM controller designed using a unique equilibrium-sliding surface by Eq. (10) to improve voltage behaviour of controller.

$$S(t) = K_p e_{rr}(t) + K_D \frac{d}{dt} e_{rr}(t) + K_i \int e_{rr}(t) dt \quad (10)$$

Where $e_{rr}(t)$ designated as a difference of reference voltage V_R and solar voltage $\frac{d}{dt} e_{rr}(t)$, $\int e_{rr}(t) dt$ represents corresponding differential and integral error, and K_p, K_i, K_D are controller parameters coefficient.

The sliding mode control is a composition of identical and discontinuous controller parts and can be represented as equation (11)

$$C(t) = C_{DIS}(t) + C_{EQ}(t) \quad (11)$$

So, from the above equation (10), to determine the sliding surface of the corresponding control signal, it is formulated and equated to 0. Therefore, equation (10) can be rewritten as

$$S = K_D e_{rr}(t) + e_{rr}(t) + K_i e_{rr}(t) = 0 \Rightarrow -K_D P_{vv} - P_{vv} + K_i e_{rr}(t) = 0 \quad (12)$$

After combining equations (8), (9), and (12), we get

$$\frac{-K_D}{C_{in}} \left[\frac{P_{vv}}{P_{vr}} - i_{load} \right] - \frac{1}{C_{in}} \left[\frac{P_{vv}}{P_{vr}} - i_{load} \right] + K_i e_{rr}(t) = 0 \quad (13)$$

$$\frac{-K_D}{C_{in}} \left(\frac{1}{C_{in} * P_{vr}} \left[\frac{P_{vv}}{P_{vr}} - i_{load} \right] \right)^{I_{C1}} - \frac{1}{L} [P_{vv} - V_o + V_o * D] - \frac{1}{C_{in}} \left[\frac{P_{vv}}{P_{vr}} - i_{load} \right]^{I_{C1}} + K_i e_{rr}(t) = 0 \quad (14)$$

Compare equation (14) with equation (11); we can get

$$C_{eq} = \frac{1}{V_o} (V_o - P_{vv} + \left(L * I_{C1} \left[\frac{1}{C_{in} * P_{vr}} + \frac{1}{K_D} \right] \right) - \left[\frac{K_i}{K_D} * L * C_{in} * e_{rr}(t) \right]) \quad (15)$$

From equation (11), it can be seen that controller signals are combined with $C_{DIS}(t)$ and $C_{EQ}(t)$. Therefore, the discontinuous $C_{DIS}(t)$ switching of the controller is assumed sigmoidal in this paper to approximate the chattering amplitude as indicated in equation (16).

$$C_{DIS} = -K_d \frac{s}{\|s\| + \varepsilon} \quad (16)$$

where K_d is a small, steady positive integer. After combining equation (15) and equation (16), we obtain equation (17), which shows the overall controller. $C_{EQ} = \frac{1}{V_o} (V_o - P_{vv} + \left(L * I_{C1} \left[\frac{1}{C_{in} * P_{vr}} + \frac{1}{K_D} \right] \right) - \left[\frac{K_i}{K_D} * L * C_{in} * e_{rr}(t) \right]) - K_d \frac{s}{\|s\| + \varepsilon}$ (17)

Stability Condition:

$$V = \frac{1}{2} s^2 \quad (18)$$

$$\dot{V} = s \dot{s} < 0$$

(19)

From equation (8) and (9)

$$P_{vv} = \frac{1}{C_{in}} \left[\frac{P_{vv}}{P_{vr}} - i_{load} \right]$$

(20)

$$(21) \quad P_{vv} = \frac{1}{C_{in}} \left[\frac{P_{vv}}{P_{vr}} - \frac{1}{L} [P_{vv} - V_o + V_o * D] \right]$$

From equation (10)

$$(22) \quad \begin{aligned} s(t) &= K_D * e_{rr}(t) + e_{rr}(t) + K_{ierr}(t) \\ &= K_D * P_{vv} - P_{vv} + K_{ierr}(t) \end{aligned}$$

Putting equation (20) in (22) and solving

$$(23) \quad \begin{aligned} s &= \frac{K_D}{C_{in}^2 * P_{vr}} \left[\frac{P_{vv}}{P_{vr}} - i_{load} \right] + \frac{K_D}{C_{in}^2 * P_{vr}} I_{C_1} + \frac{1}{C_{in}} I_{C_1} - K_{ierr}(t) \\ &= -\frac{K * K_D * V_o}{L * C_{in}} \left(\frac{s(t)}{\|s(t)\| + \varepsilon} \right) - \frac{1}{C_{in}} \left[\frac{P_{vv}}{P_{vr}} - i_{load} \right] + K_{ierr}(t) \end{aligned}$$

Where, $I_{C_1} = C_{in}$

$$P_{vv} = \frac{1}{C_{in}} \left[\frac{P_{vv}}{P_{vr}} - i_{load} \right]$$

So equation (23) can be rewritten as

$$(24) \quad s = -K \left[\frac{K_D * V_o}{L * C_{in}} \right]^m * \frac{s}{\|s\| + \varepsilon}$$

From equation (24) value of m is always positive. So the sign of the equation depends on the sign of K. So, s considered as

$$(25) \quad s = -K * \frac{s}{\|s\| + \varepsilon}$$

Putting equation (25) into (19), we can get

$$(26) \quad V = ss = \left\{ -K \left(\frac{s^2}{\|s\| + \varepsilon} \right) \right\} < 0$$

$$\left(\frac{s^2}{\|s\| + \varepsilon} \right) > 0$$

$$-K < 0$$

From equation (26), the value of K should be positive for convergence.

4. Power flow control (PFC)

This section covers the power flow controller architecture and logic that controls the operating modes of a grid-tied single-phase inverter. The below flowchart demonstrates the power flow control strategy for maintaining constant voltage in the bus.

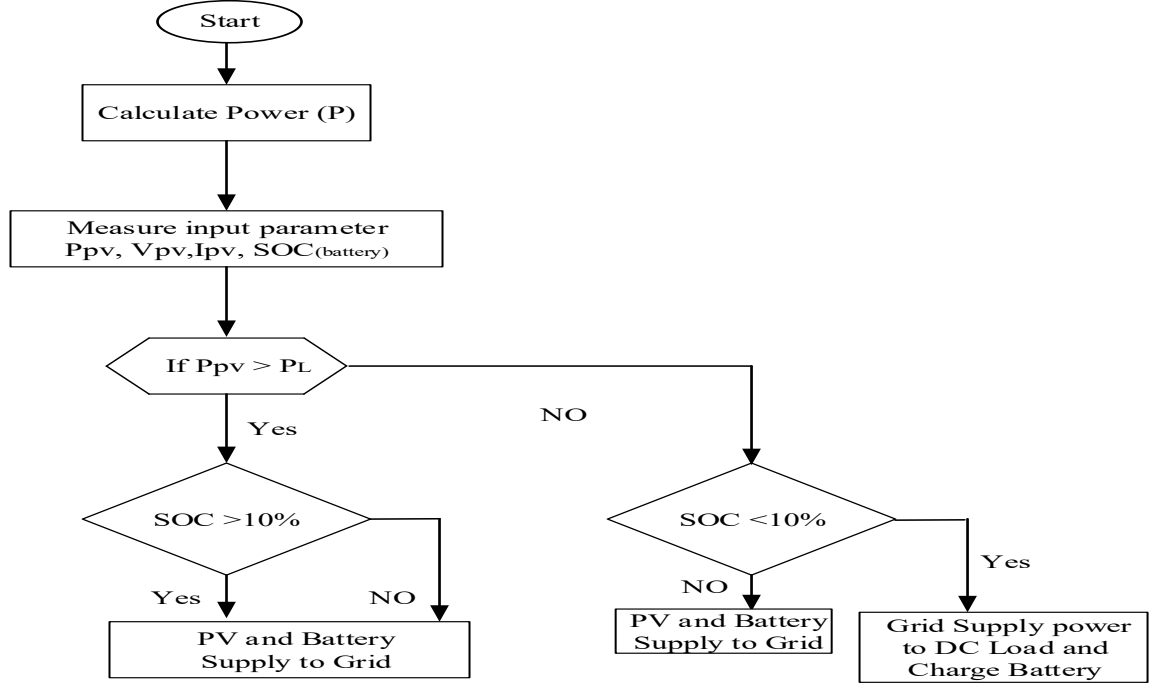


Fig. (3)Control logic for power flow management control

As earlier mentioned, the system operates with a grid-tied single-phase inverter, where the network-determined reference value. Therefore By converting the reference frames of ac waveforms into dc signals, the DQ transformation is used for the reference signal generation. After that, the inverse transform is used to acquire accurate reference signals. The DQ transformation is represented in equation (27) in matrix form, and 'Z' indicates voltage or current.

$$\begin{bmatrix} Z_d \\ Z_q \end{bmatrix} = \begin{bmatrix} \sin \sin(w_0 t) & -\cos \cos(w_0 t) & \cos \cos(w_0 t) & \sin \sin(w_0 t) \end{bmatrix} \begin{bmatrix} Z_\alpha \\ Z_\beta \end{bmatrix} \quad (27)$$

An imaginary phasor 90 degrees out of phase with reference input is created, enabling DQ transformation. By using Park's transformation method, the DQ frame rotates synchronously with inverter voltage for angular speed as given in equation (28)-(33)

$$\frac{di_{id}}{dt} = \frac{-R_f}{L_f} i_{id} + w_0 i_{qL} + \frac{1}{L_f} (v_{id} - v_{dc}) \quad (28)$$

$$\frac{di_{qL}}{dt} = \frac{-R_f}{L_f} i_{iq} + w_0 i_{dL} + \frac{1}{L_f} (v_{iq} - v_{qc}) \quad (29)$$

$$\frac{dv_{dc}}{dt} = w_0 v_{dc} + \frac{1}{C_f} (i_{id} - i_{dL}) \quad (30)$$

$$\frac{dv_{qc}}{dt} = -w_0 v_{dc} + \frac{1}{C_f} (i_{iq} - i_{qL}) \quad (31)$$

$$\frac{di_{iL}}{dt} = \frac{-R_g}{L_g} i_{dL} + w_0 i_{qL} + \frac{1}{L_f} (v_{dc} - v_{dL}) \quad (32)$$

$$\frac{di_{qL}}{dt} = \frac{-R_g}{L_g} i_{gL} + w_0 i_{dL} + \frac{1}{L_f} (v_{qc} - v_{qL}) \quad (33)$$

Now for controlling active and reactive power, equation (34)-(35) used

$$P = v_d i_d + v_q i_q = v_d i_d$$

$$Q = -(v_d i_q + v_q i_d) = -v_d i_q$$

To synchronize the inverter and grid voltage, a phase-locked-loop (PLL) is used. This is accomplished by figuring out the grid current along with the phase angle of the grid voltage. The current controller regulates the grid current I_g using a reference set by the voltage controller. For obtaining the pure sine wave with 350 V amplitude, an LCL filter is used to filter out the harmonics in the inverter output waveform

5. Results and discussion

The research described in this section was carried out using MATLAB Simulink software. The MATLAB Simulink software tool is used for modeling, simulating, and analyzing system dynamics. In the proposed study

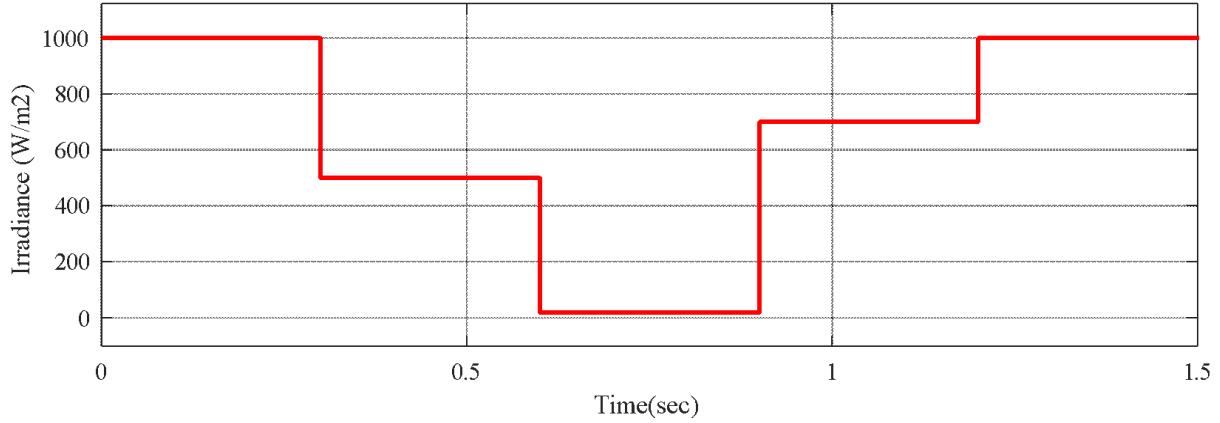


Fig. (4) Solar insolation profile

varied solar insolation profiles in trapezoidal shapes that represent the amount of solar radiation reaching a specific solar region were examined. The solar insolation ranges from 20 to 1000 W/m² are depicted in Fig (4) and divided into five stages, namely 500, 20, 700, and 1000 W/m² at a constant temperature, i.e., 25°C. The deviation in solar insolation is considered at 0.3sec, 0.6sec, 0.9sec, and 1.2sec, respectively. Kyocera KC200GT, under standard test scenarios, has been used in this research for solar power generation.

[Image could not be embedded — unsupported format][Image could not be embedded — unsupported format] This research aims to look into the performance of the suggested method in response to variations in solar insolation and provide insights into how to improve the efficiency of solar PV output and obtain the maximum possible power from the PV.

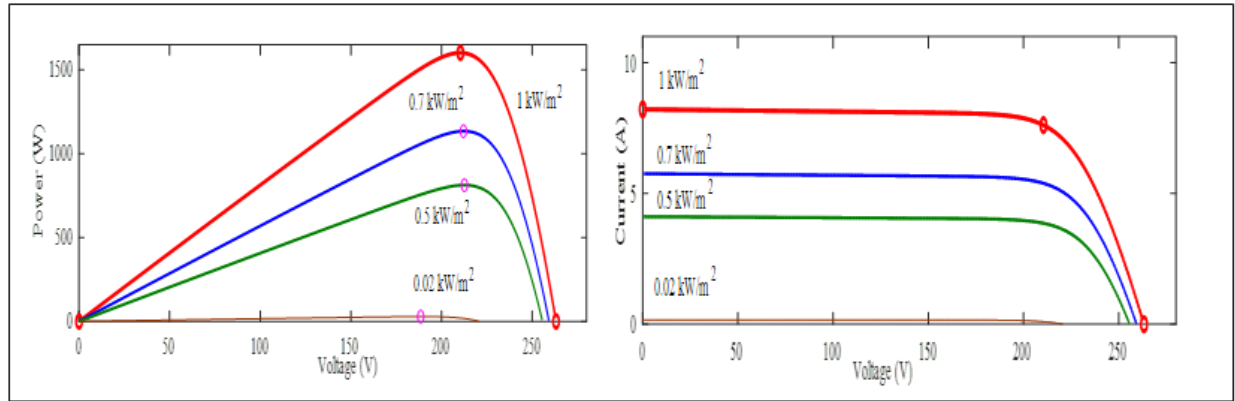


Fig. (5) Solar PV (a) PV characteristics

(b) IV characteristics

Figures 5(a) and 5(b) depict the I-V and P-V of the employed Kyocera photovoltaic KC200GT for different temperature and insulation profiles.

In this article, to maximize the power output of a PV array by using three different MPPT techniques: P&O, InC, and POASMC. The performance of these methods was evaluated in both transient and steady-state conditions, under varying climatic conditions, by analyzing the array's voltage, current, and power output. The results are presented in Figures 6(a-c), and a detailed analysis of efficiency, dynamic power output variation, and settling time for each technique is provided in Tables 1 and 2. Overall, the study compares and assesses the effectiveness of these three distinct MPPT approaches.

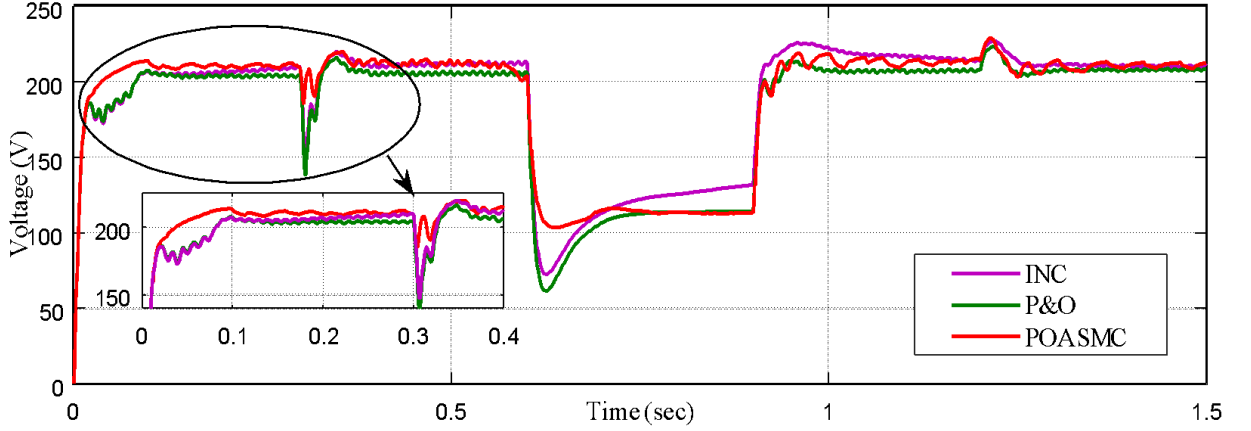


Fig. (6-a) Performance comparison of output voltage

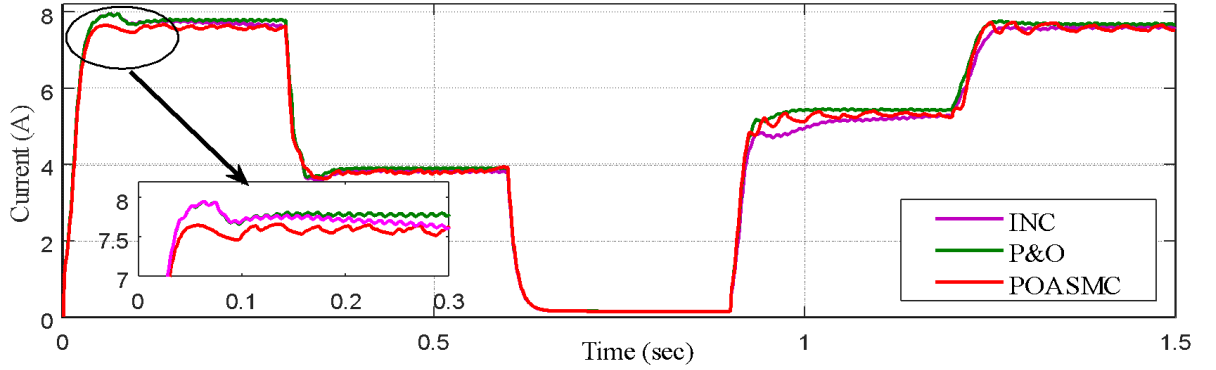


Fig. (6-b) Performance comparison of output current

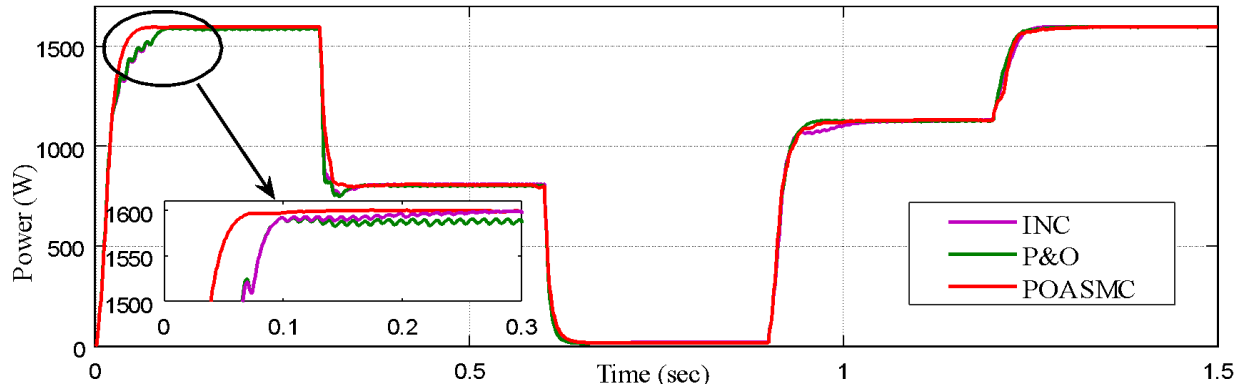


Fig. (6-C)Performance comparison of output power**Table 1: Performance comparison of efficiency for InC, P&O, and proposed POASMC techniques**

		Output Power (Achieve)			Efficiency		
Irradiance	Solar output(Ideal)	InC technique	P&O technique	POASMC Proposed	InC technique	P&O technique	POASMC Proposed
20 W/m ²	28.83W	19.1W	18W	19.2W	66.25	62.43	66.59
500 W/m ²	812W	810W	804W	810W	99.75	99.01	99.75
700 W/m ²	1134W	1128W	1128W	1131W	99.47	99.47	99.73
1000 W/m ²	1601W	1590W	1586W	1600W	99.31	99.06	99.93

The implemented MPPT method successfully monitors the MPP of solar production in all four studied states, as depicted in Fig. 6(a-c) and Table 1-2. Table 1 shows that the fluctuations near the MPP create a sizable ripple in output power when P&O MPP techniques are employed, as shown in Fig. 6c. As a result, the solar PV array's total output power decreases. Additionally, the use of the InC MPP algorithm results in a significant decrease in ripple content in P_{pv}. However, this method cannot operate MPP during steady-state conditions. In contrast, the proposed POASMC MPP technique can effectively track the maximum power generated by solar energy with a tracking efficacy of 99.93%, with hardly any ripple content.

Table 2: Performance comparison and analysis of settling time and output power variation in InC, P&O, and proposed POASMC techniques

		Output power settling Time			Variation in Output		
Insulation	Solar output (Ideal) Voltage	InC technique	P&O technique	POASMC Proposed	InC technique	P&O technique	POASMC Proposed
1000 W/m ²	188.6 V	0.1	0.13	0.07	1588-1598W	1584-1590W	1598-1601W
500 W/m ²	212.7 V	0.36	0.36	0.325	755-810W	770-810W	800-810W
20 W/m ²	211.6 V	0.75	0.75	0.7	16.6-21.5W	15.2-19.1W	18-19.2W
700 W/m ²	210.4 V	1.04	1.05	0.98	1124-1128W	1125-1130W	1128-1132W

Furthermore, when comparing the POASMC technique to the P&O and InC techniques for transient performance evaluation, as illustrated in Fig 6 (a-c) and Table(2), the POASMC scheme has a fairly decent response. For achieving maximum solar output when P&O technique used the output responses settling time 0.13sec, 0.36sec, 0.75sec, 1.05sec for 1000 W/m², 500 W/m², 20 W/m² and 700 W/m² irradiance respectively. Similarly, when the InC technique is used, the settling time for tracking the output power is 0.1sec, 0.36sec, 0.75sec, and 1.04sec, with decreased ripple content in power output for the same irradiance. The PV system employing the POASMC technique shows nearly little ripple content in PV with the minimal settling time of 0.07sec, 0.325sec, 0.7sec, and 0.98sec for 1000 W/m², 500 W/m², 20 W/m², 700 W/m².

According to the results and extensive research, the suggested POASMC MPPT scheme produces excel results than the P&O and InC approaches in terms of transient and steady-state performance, i.e., more excellent tracking,

reduced ripple at the output, and lower settling time. As a result, the suggested POASMC technique has been studied and deployed to track MPP efficiently.

5.1 Grid-connected Solar PV power flow result

A grid-connected solar PV system and battery as storage with inverter operation are discussed in this section. For the combination of 1600W solar PV power with a single-phase grid, the suggested POASMC MPPT controller's performance has been evaluated regarding grid-side metrics such as the quality of the injected current under periodically changing solar insolation. Variations in solar insolation cause variations in the maximum energy extracted from the solar PV array, as illustrated in Fig. (4). This variation in generated power impacts the constant power supply to the DC bus connected load on battery SOC and the amount of power flow between solar PV and the grid. From Fig 8(c) and 13(c), it can be spotted that I_g (reference current) changes by the change in solar irradiance. So for that, in this article, logic has been designed based on solar PV power and the SOC of the battery in such a way that when solar PV generates access to power, and the SOC of the battery is sufficiently charged generated access power flow in grid side with maintained constant power supply to the load.

Furthermore, if generated power of solar PV is less than the load demand and the SOC of the battery is less than a certain level. The grid provides a power supply to the DC load and charges the battery. To explain this scenario, two conditions have been considered for power flow management.

CASE I: when the SOC of the battery is 50% and the input current is greater than the reference current:

In the first condition, When the SOC of the battery has been considered at 50%, and the solar irradiance is 1000W/m². In this condition (for 0-0.3sec), solar PV generates 1602W output power. In this scenario, when solar PV produces enough power to keep the DC bus voltage at 350V, then access Solar developed power charges the battery as shown in Fig (7) and Fig (10-b).

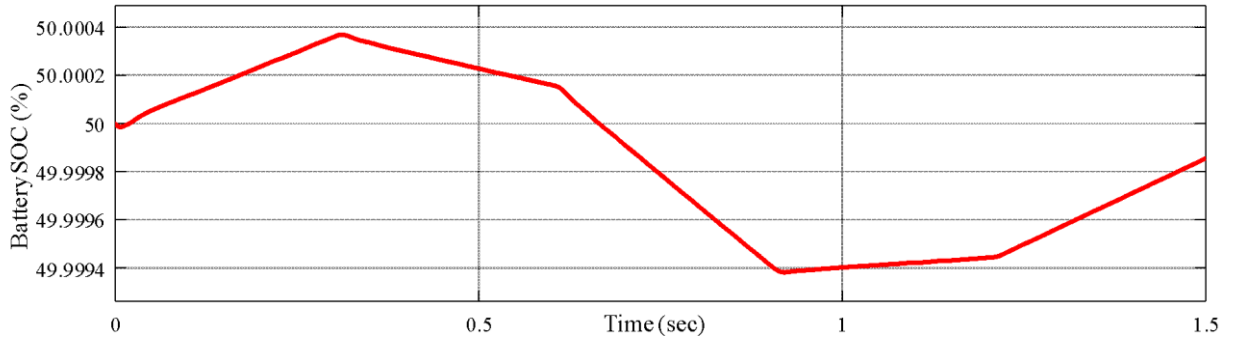


Fig. (7) Battery SOC in case I

It was evident from Fig (10b) that the charging current for the battery is -2 amp. As a result, the 0-0.3sec battery operates in charging mode, and the battery SOC increases. At 0.3 sec, when solar PV irradiance shifts from

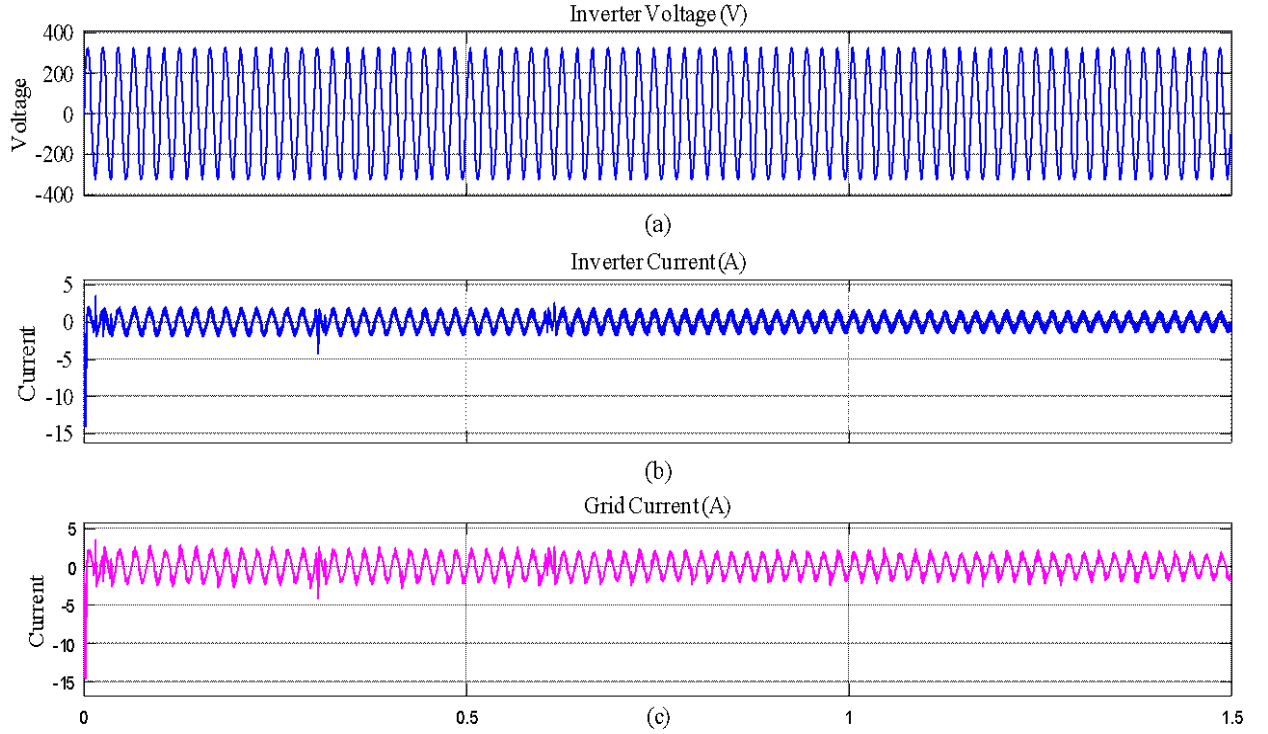


Fig. (8) Case I (a) Inverter Voltage (b) Inverter current (c) Grid current

1000w/m² to 500 w/m², solar PV generates 812W, as seen in Fig (6c) and Table(1). As shown in Fig (0.3sec-0.6sec), the PV converter current drops from 4A to 2A at this instance (9a). Therefore, to maintain a constant power supply to the DC bus, the battery current switches from negative to positive, and the battery operates in charging to discharging mode to support the continuous power supply to the DC bus, i.e., 350V. Thus, the direction of current movement changes from the solar PV side to the grid side. From 0.6 sec to 0.9 sec, when solar irradiance is 500 w/m² to 20 w/m², solar power cannot produce enough (approximately 28 watts) to keep the DC bus voltage constant. As a result, battery-discharging capacity increases to maintain the DC bus voltage constant, as depicted in Fig (10b) and Fig (7). After 0.9 sec, when solar power increases from 28W to 1134W while increasing solar irradiance rises from 20 w/m² to 700 w/m², the battery current switches from positive to negative, i.e., discharging to charging mode. In this mode, since solar generates sufficient power to charge the battery and maintain DC bus voltage, the generated access power will flow on the grid side from solar PV.

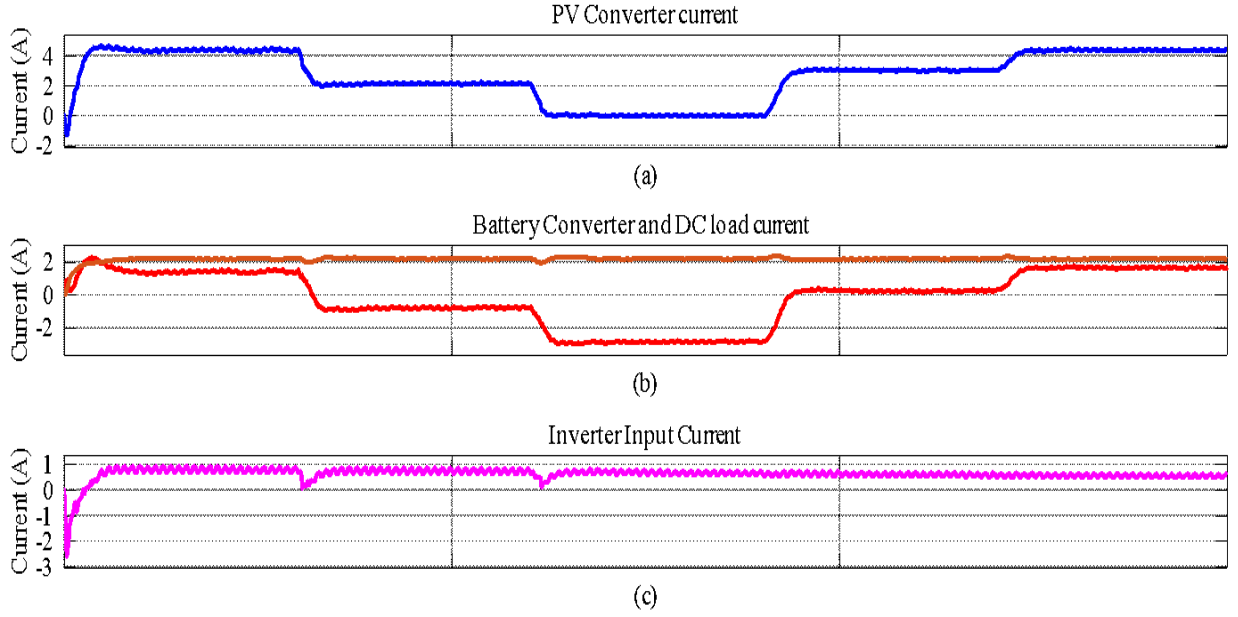


Fig. (9)Case I(a) Solar PV output current (b) Battery converter and load current (c) Input inverter current

Fig (8) shows the inverter voltage, inverter current, and grid current. It is evident from Fig (8) that the grid side

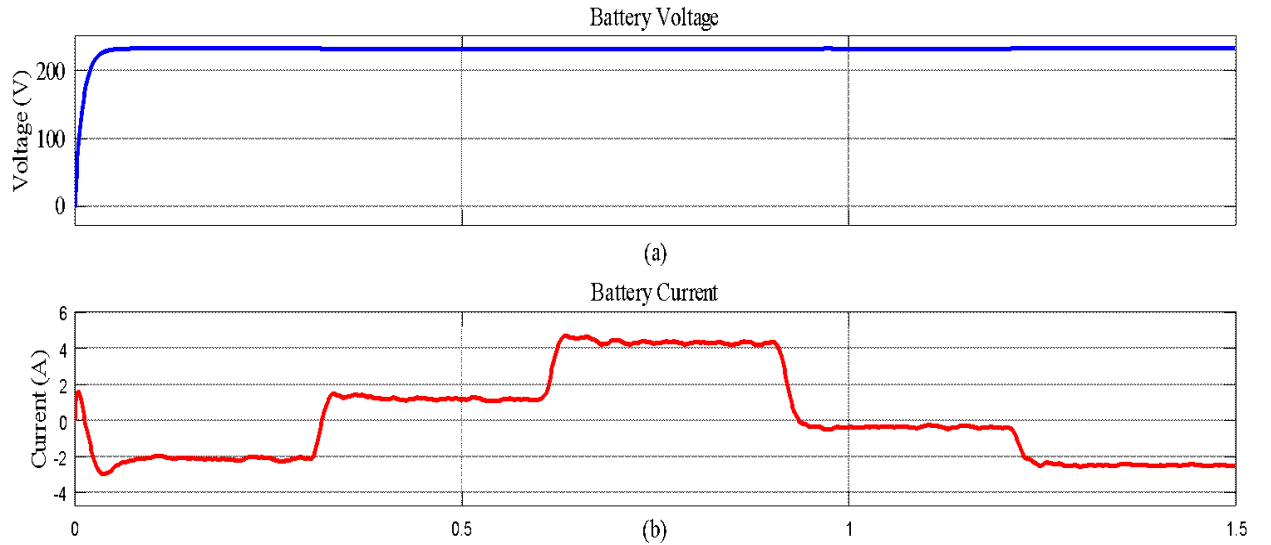


Fig. (10)Case I(a) Battery voltage (b) Battery current

is receiving the accesses power. Thus, the current flow is from solar PV and battery to the grid. Fig (9b) depicts the Battery converter and load current, Fig (9c) inverter input current, Fig (10a) battery voltage, and Fig (11a-c) DC load current, Load voltage, and load power, respectively.

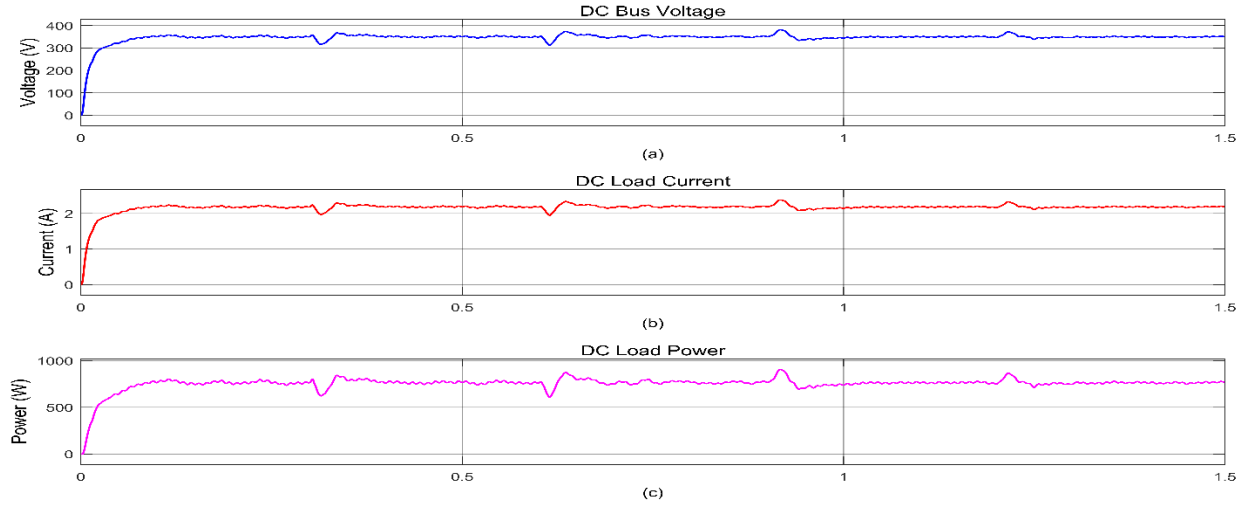


Fig.(11)Case I(a) DC load voltage (b) DC load current (c) DC load power

CASE II: when the SOC of the battery is less than 10%, and the charging current is less than the reference current:

In the second case, when solar irradiance is 1000 w/m², and the SOC of the battery is less than 10% (i.e., 9.98%). In this condition (0-3sec), solar PV generates 1602W output; it effectively supplies the power to the DC load to maintain bus voltage at 350V. As a result, the battery works in charge mode, as shown in Figures (12) and (14-a).

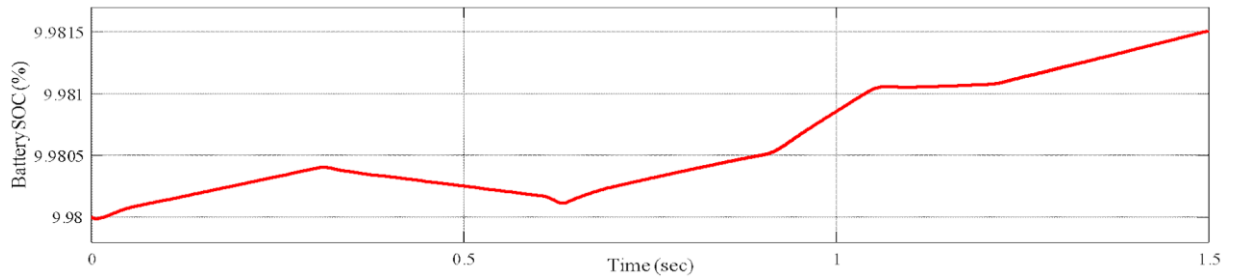


Fig.(12)Battery SOC in case II

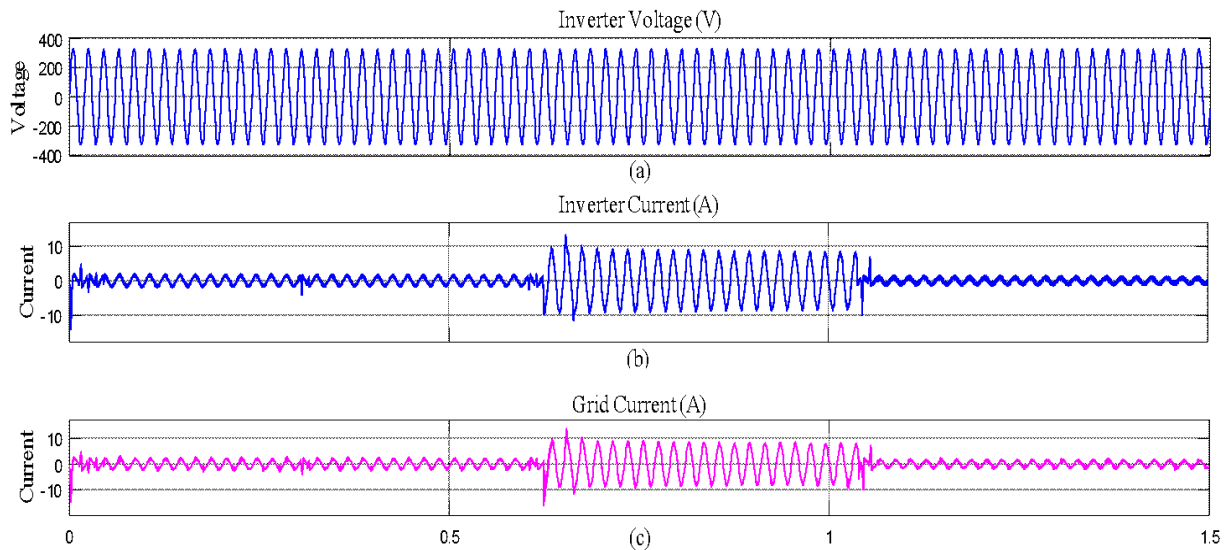


Fig. (13)Case II (a) Inverter Voltage (b) Inverter current (c) Grid current

After 0.3 sec to 0.6 sec, when solar irradiance changes from 1000 w/m² to 500 w/m², the generated solar output becomes 512 watts. To maintain the DC bus voltage at 350 V, the battery operating mode will change from charging to discharging, as shown in Fig (12). To provide load current efficiently, the battery works in discharging mode. Furthermore, after 0.6sec to 0.9 sec, irradiance falls to 20 w/m², and solar power generates approx. 28W. In this case, the SOC of the battery is still less than 10%. So, to maintain the DC bus voltage constant, the grid needed to supply power to charge the battery and maintain the DC bus voltage at 350V. After 0.9 sec, when irradiance changes from 20 w/m² to 700 w/m², Solar generates 1134W, but battery SOC is still less than 10%. Since the battery SOC is less than the then desired level, it still receives the current from the grid until the battery SOC reaches 9.981%. The power flow in this mode will be from the grid to the solar PV side. It is clearly shown in Fig (13) that when battery SOC is less than 10%, and solar irradiance is 20 w/m² grid supplies the current to the inverter with 10A.

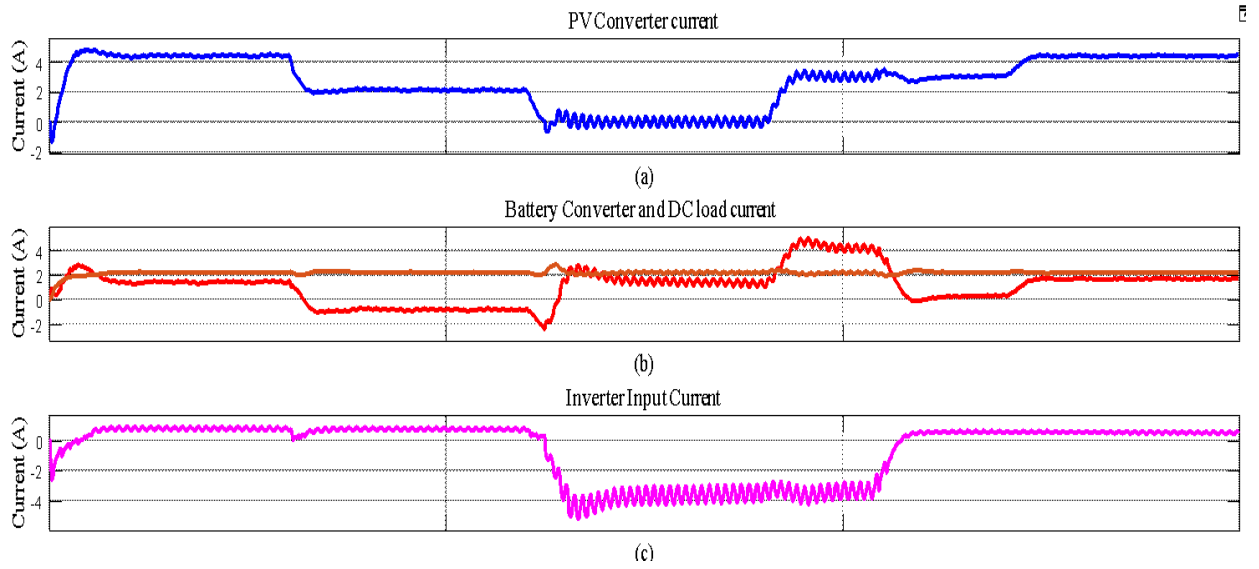


Fig. (14)Case II(a) Solar PV output current (b) Battery converter and load current (c) Input inverter current

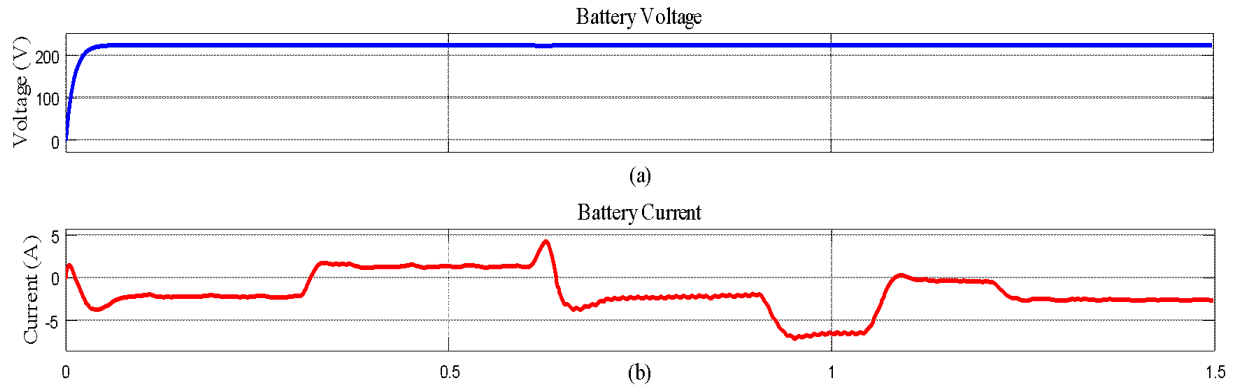


Fig. (15)Case II(a) Battery voltage (b) Battery Current

At 1.2 sec, when solar irradiance changes from 20 w/m² to 700 w/m², solar power output becomes, 1602 watts, and battery charging SOC is greater than 9.981%. Then inverter current flow logic changes, solar PV supplies the power to the DC bus load, and the power flow becomes solar PV to the grid side. As a result, the grid and inverter current flow becomes 2A, as shown in Fig (13). Fig (15) and Fig (16) shows the battery voltage, battery current, DC load voltage, load current, and load power, respectively, for different solar irradiance level.

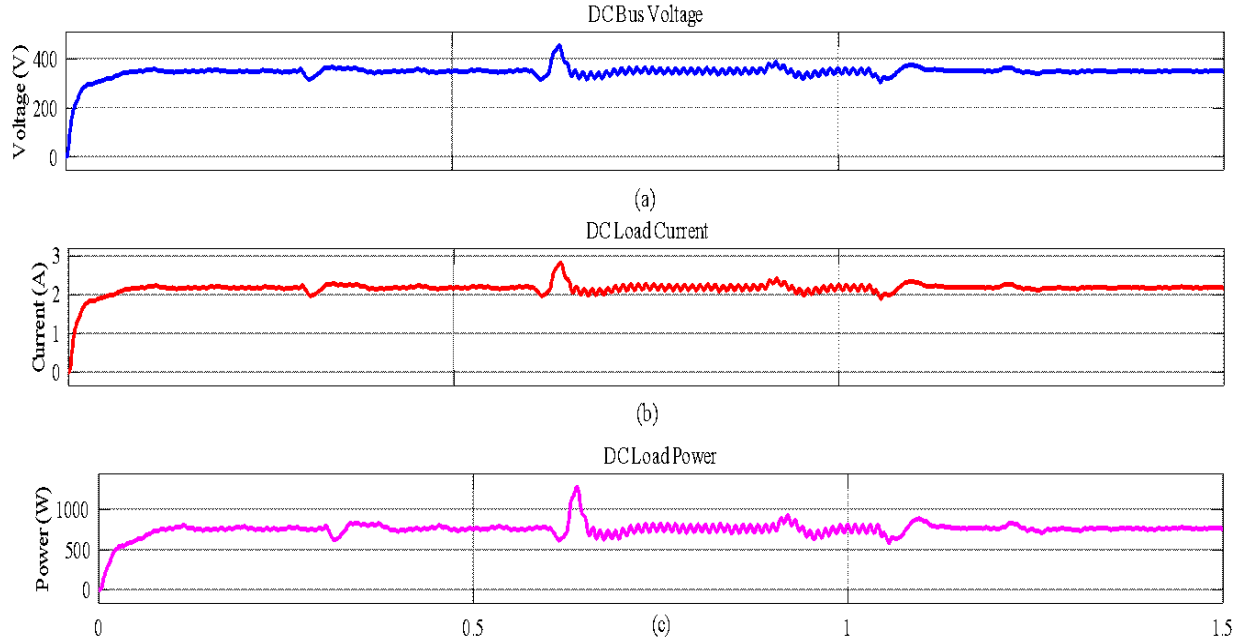


Fig.(16)Case II(a) DC bus voltage (b) DC load current (c) DC load power

6. Conclusion

In conclusion, this article provides a framework for implementing a solar PV, battery, and grid-connected charging station, along with a critical analysis of its performance. The proposed power flow control logic for energy management addresses load demand uncertainty under different solar irradiances. The research highlights the importance of considering the fluctuation of renewable sources and synchronization with the grid system when developing energy management algorithms for renewable energy systems. So for that, two conditions have been considered. First, when the battery's SOC is 50%, and the charging current is greater than the reference current, and second If the battery's SOC is lower than 10% and the charging current lower than the reference current. The PI controller is designed to synchronize solar PV with the grid and battery, while the POASMC MPPT control algorithm is suggested for grid-connected solar PV systems. The study results show that the proposed POASMC MPPT provides improved tracking efficiency with a value of 99.93%, effective faster convergence, with minor deviation from MPP under stochastic solar irradiance compared to traditional P&O and InC. These findings suggest that the proposed framework and techniques can effectively address the challenges of implementing energy management algorithms in renewable energy systems. The research highlights the importance of considering the fluctuation of renewable sources and synchronization with the grid system when developing energy management algorithms for renewable energy systems.

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